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> Amendment Transmittal in duplicate Response under 37 CFR 1.111 and 1.143

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MAR 0 8 2004

Fax: Phone:	703-872-9306 Victor W. C. Chan, et al.	Pages: Date:	7 including cover March 8, 2004	
Re:	U.S. Patent Appln. No. 10/604,003 Our Docket: FIS920030105US1 (16462)	CC:		
XX Urg	ent 🗆 For Review 🗀 Please Con	nment	☐ Please Reply	☐ Please Recycle
Dear Sir	÷			
The foll	lowing is attached hereto:			•

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CERTIFICATE OF		Docket No.							
Applicant(s): Victor W.	C. Chan, et al.	·	F	IS920030105US1 (16462)					
Serial No.	Filing Date	Examiner		Group Art Unit					
10/604,003	6/20/2003	F. Erdem	1	2826					
Invention: SUBSTRATE ENGINEERING FOR OPTIMUM CMOS DEVICE PERFORMANCE									
I hereby certify that this	RES	PONSE UNDER 1.111 AND 1.	.143						
is being facsimile transm	nitted to the United States Patent	(Identify type of correspondence) and Trademark Office (Fax.)	No. <u>7</u>	03-872-9306					
on March 8,	2004								
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		Leslie S. Szjv (Typed or Printed Jume of Pers	gs, Ph.	D. ing Certificate)					
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AMENDMENT TRANSMITTAL LETTER (Large Entity) Applicant(s): Victor W.C. Chan, et al.							Docket No. FIS920030105US1 (16462)		
Serial No. 10/604,003		Filing Date 6/20/2003		Examiner • F. Erdem			Group Art Unit 2826		
Invention: SUBSTRATE ENGINEERING FOR OPTIMUM CMOS DEVICE PERFORMANCE									
	TO THE COMMISSIONER FOR PATENTS: Transmitted herewith is an amendment in the above-identified application.								
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		EMAINING MENDMENT	HIGHEST#	i	MBER EXTRA	F		ADDITIONAL	
TOTAL CLAIMS	17	-	PREV. PAID FOR 20 =		IIMS PRESENT 0	×	\$18.0	FEE \$0.00	
INDEP. CLAIMS	2	-	3 =			X.	\$84.0		
Multiple Depender	nt Claims (check if appl	icable)			1		\$0.00	
		<u> </u>	TOTAL ADDITIO	NAL FEE	FOR THIS AM	ENDI	MENT	\$0.00	
No additional fee is required for amendment. ☐ Please charge Deposit Account No. in the amount of ☐ A check in the amount of to cover the filing fee is enclosed. ☐ The Director is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. ☐ Any additional filing fees required under 37 C.F.R. 1.16. ☐ Any patent application processing fees under 37 CFR 1.17. ☐ Dated: March 8, 2004 Customer No. 23389 ☐ Certify that this document and fee is being deposited on with the U.S. Postal Service as first class malt-under 37 C.F.R. 1.8 and is addressed to the Commissioner for Patents, P.O. Box 1950, Alexandria, VA									
Signature of Person Mailing Correspondence Typed or Printed Name of Person Mailing Correspondence									

CERTIFICATE OF Applicant(s): Victor W.	Docket No. FIS920030105US1 (16462)								
Serial No. 10/604,003	Filing Date 6/20/2003	Group Art Unit 2826							
Invention: SUBSTRATE ENGINEERING FOR OPTIMUM CMOS DEVICE PERFORMANCE									

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AMENDMENT TRANSMITTAL LETTER (Large Entity) Applicant(s): Victor W.C. Chan, et al.							Docket No. FIS920030105US1 (16462)	
Serial No. 10/604,003		Filing Date 6/20/2003		Examiner F. Erdem			Group Art Unit 2826	
Invention: SUBSTRATE ENGINEERING FOR OPTIMUM CMOS DEVICE PERFORMANCE								
	TO THE COMMISSIONER FOR PATENTS: Transmitted herewith is an amendment in the above-identified application.							
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INDEP. CLAIMS	2	· -	3 =		0	x \$8	34.00	\$0.00
Multiple Depender	t Claim	s (check if appl	icable)					\$0.00
	TOTAL ADDITIONAL FEE FOR THIS AMENDMENT \$0.00							
 No additional fee is required for amendment. □ Please charge Deposit Account No. in the amount of □ A check in the amount of to cover the filing fee is enclosed. ☒ The Director is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. ☒ Any additional filing fees required under 37 C.F.R. 1.16. ☒ Any patent application processing fees under 37 CFR 1.17. ☐ Dated: March 8, 2004 Leslie S. Szivos, Ph.D. Registration No. 39,394 								
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Victor W.C. Chan, et al.

Examiner: F. Erdem

Serial No:

10/604,003

Art Unit:

2826

Filed:

June 20, 2003

Docket: FIS920030105US1 (16462)

For: SUBSTRATE ENGINEERING FOR

Dated:

March 8, 204

OPTIMUM CMOS DEVICE PERFORMANCE

Commissioner for Patents United States Patent Office Alexandria, VA 23313-1450

RESPONSE UNDER 37 C.F.R. §§1.111 AND 1.143

Sir:

In response to the Office Action dated February 19, 2004, applicants provisionally elect, without traverse, the subject matter of Group II, i.e., Claims 11-17, for continued prosecution in the above-identified patent application.

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that this paper is being facsimile transmitted to the Patent and

Trademark Office on the date shown below.

Dated: March 8, 2004

Leslie S. Szivos, Ph.D.

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REMARKS

Favorable consideration and allowance of the claims of the present application are respectfully requested.

In the present Official Action, Claims 1-17 which are present in the above-captioned application have been subjected to a restriction under 35 U.S.C. §121 as follows:

- Claims 1-10, drawn to a method of making a semiconductor device,
 classified in Class 438, subclass 198; and
- II. Claims 11-17, drawn to a semiconductor device, classified in Class257, subclass 627.

As indicated, and in order to be fully responsive to the restriction requirement imposed in the present Official Action, applicants provisionally elect, without traverse, to prosecute the subject matter of Group II, i.e., Claims 11-17, and reserve the right to file one or more divisional applications directed to the non-elected subject matter in this application, i.e., Claims 1-10.

Wherefore, consideration and allowance of the claims of the present application are respectfully requested.

Respectfully submitte

Lessie S. Szivos, Ph.D. Registration No. 39,394

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LSS/sf